# International Rectifier

# HiRel™ INT-A-Pak 2, PLASTIC HALF-BRIDGE IGBT MODULE

### **G450HHBK06P2**

**Product Summary** 

Part Number	V <sub>CE</sub>	Ic	V <sub>CE(SAT)</sub>	
G450HHBK06P2	600V	450A	1.8	



The HiRel™ INT-A-Pak series are isolated near hermetic power modules which combine the latest IGBT and Soft Recovery Rectifier Technology. The module uses both high-speed and low Vce(sat) IGBT's packaged for ultra low thermal resistance junction to case. The G450HHBK06P2 power module consists of six IGBT's and six FRED's in a Phase- Leg or Half-Bridge configuration.

#### Features:

- Rugged, Lightweight near Hermetic Package with Integrated Power Terminal Cap
- Gen IV IGBT Technology
- Soft Recovery Rectifiers
- Ultra-Low Thermal Resistance
- Zener Gate Protection
- Very Low Conduction and Switching Loss
- -55°C to +125°C Operating Temperature
- Screening to meet the intent of MIL-PRF-38534 Class H
- Short Circuit Capability
- 2.0 Ohms Series Gate Resistor
- High Altitude Operation, 85,000 Feet Above Sea Level at Rated Voltage

# Absolute Maximum Ratings @ Tj=25°C (unless otherwise specified)

Parameter	Symbol	Value	Units	
Collector-to-Emitter Voltage	V <sub>CES</sub>	600		
Gate-to-Emitter Voltage	V <sub>GE</sub>	±20	]	
Continuous Collector Current @ Tc = 25°C		600		
Continuous Collector Current @ Tc = 70°C		450	1 ^	
Isolation Voltage	V <sub>ISOL</sub>	2500	V <sub>RMS</sub>	

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# Electrical Characteristics @ Tj = 25°C (unless otherwise specified)

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Off Characteristics						
Collector Emitter Breakdown Voltage	V <sub>CES</sub>	V <sub>GE</sub> = 0V	600	-	-	V
Zero Gate Voltage Collector Current	ICES	V <sub>GE</sub> =0V, V <sub>CE</sub> = 600V	-	-	2.0	mA
Gate Emitter Leakage Current	I <sub>GES</sub>	V <sub>GE</sub> = ±15V, V <sub>CE</sub> = 0V	-	-	10	μA
On Characteristics		- 10 (II - 101)				
Gate Threshold Voltage	V <sub>GE(TH)</sub>	V <sub>CE</sub> = V <sub>GE</sub> , I <sub>C</sub> = 45mA	4.0	-	7.5	V
Collector Emitter Saturation Voltage		V <sub>GE</sub> = 15V, I <sub>C</sub> = 450A		1.8	2.6	
Dynamic Characteristics						
Total Gate Charge	Qg	V <sub>CF</sub> = 300V, I <sub>C</sub> = 450A, V <sub>GF</sub> = 15V		2,600		nC
Input Capacitance	C <sub>IES</sub>	VCE - 300V, IC - 430A, VGE - 13V		48		nc nc
Output Capacitance	C <sub>OES</sub>	V <sub>GE</sub> = 0V, V <sub>CE</sub> = 25V, f = 1.0MHz		3.0	-	nF
Reverse Transfer Capacitance	C <sub>RES</sub>			0.3		
Noveled Trailorer Capacitation	- 7.53	· · · · · · · · · · · · · · · · · · ·		0.0		
Switching Inductive Load Chara	acteristic	s				
Turn-On Delay Time	td(on)		-	800	900	ne
Rise Time	tr			460	700	ns
Turn-On Losses	Eon	V <sub>CC</sub> = 300V, I <sub>C</sub> = 450A, V <sub>GE</sub> =15V	-	45	•	mJ
Turn-Off Delay Time	td(off)	$R_{G(on)} = 20\Omega$ , $R_{G(off)} = 10\Omega$ , L=100µH		2800	3400	
Fall Time	tf		-	400	500	ns
Turn-Off Losses	E <sub>off</sub>		-	60	-	mJ
Diode Characteristics						
Forward Voltage	V <sub>F</sub>	I <sub>F</sub> = 450A	-	1.2	1.8	٧
Reverse Recovery Charge	Оп		-	9.5	12	μC
Peak Reverse Recovery Current	Irr	V <sub>R</sub> =300V, I <sub>C</sub> =450A, di/dt =-1100A/μs	-	105	-	A
Reverse Recovery Time	trr			160	170	ns

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Electrical Characteristics @ Tj = 125°C (unless otherwise specified)

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Off Characteristics	<u> </u>					
Collector Emitter Breakdown Voltage	V <sub>CES</sub>	V <sub>GE</sub> = 0V	600	+	-	٧
Zero Gate Voltage Collector Current	I <sub>CES</sub>	V <sub>GE</sub> =0V, V <sub>CE</sub> = 600V	-	-	18	mA
Gate Emitter Leakage Current	I <sub>GES</sub>	V <sub>GE</sub> = ±15V, V <sub>CE</sub> = 0V	-	-	10	μΑ
On Characteristics Gate Threshold Voltage	V <sub>GE(TH)</sub>	V <sub>CE</sub> = V <sub>GE</sub> , I <sub>C</sub> = 45mA	4.0	-	7.5	
On Characteristics		104 - 145 - 45 - 4	1			
Collector Emitter Saturation Voltage		V <sub>GE</sub> = 15V, I <sub>C</sub> = 450A	-	1.8	2.6	V
Diada Charactadalas			•			
Diode Characteristics						
Forward Voltage	V <sub>F</sub>	I <sub>F</sub> = 450A	-	1.2	1.8	V

# **Thermal-Mechanical Specifications**

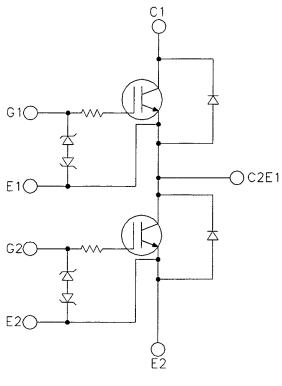
Parameter	Symbol	Min	Max	Units
IGBT Thermal Resistance, Junction to Case, per Switch		-	0.07	·cw
Diode Thermal Resistance, Junction to Case, per Switch	- R <sub>tNC</sub>	-	0.12	]
Operating Junction Temperature Range	TJ	-55	150	•c
Storage Temperature Range	T <sub>STG</sub>	-55	125	1
Screw Torque - Mounting			26	in-lbs
Screw Torque - Terminals	<u>'</u>	•	20	111-105
Module Weight		-	270	g

#### **Module Screening**

Test or Inspection	MIL-STD-883		Comments
	Method	Condition	
Internal Visual	2017		
Temperature Cycle	1010	В	10 Cycles, -55°C to +125°C
Mechanical Shock	2002	В	1500G, 0.5ms, 5 Times (Y1 direction only)
Burn-in	1015	Α	160 Hrs @ +125°C
Final Electrical Test			Group A, -55°C, +25°C, +125°C
External Visual	2009		

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# **Schematic**



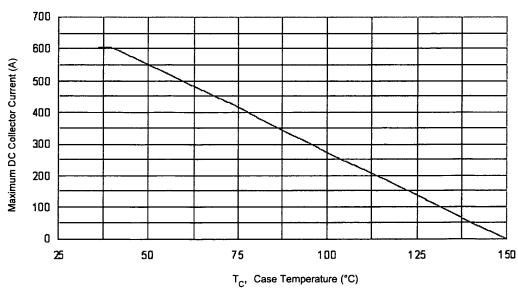
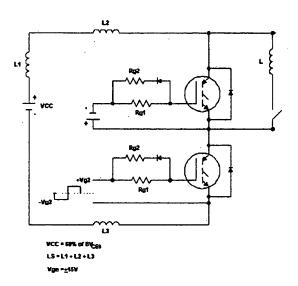


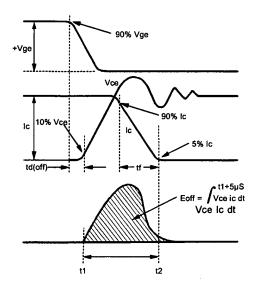
Fig 1: Maximum Collector Current Vs Case Temperature

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 $\begin{aligned} \textbf{Fig. 2} & - \text{Test Circuit for Measurement of } & E_{\text{on}}, \\ & E_{\text{off}}, t_{\text{rr}}, Q_{\text{rr}}, I_{\text{rr}}, t_{d(\text{on})}, t_{\text{r}}, t_{d(\text{off})}, t_{\text{f}} \end{aligned}$ 



 $\mbox{\bf Fig. 3 - Test Waveforms for Circuit of Fig. 2,} \\ \mbox{\bf Defining } \mbox{\bf E}_{\mbox{off}}, \mbox{\bf t}_{\mbox{d}(\mbox{off})}, \mbox{\bf t}_{\mbox{f}}$ 

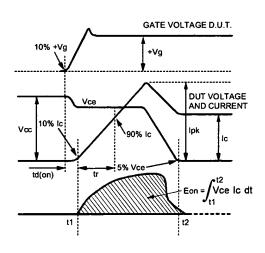
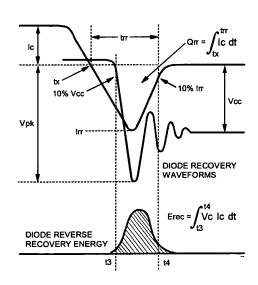
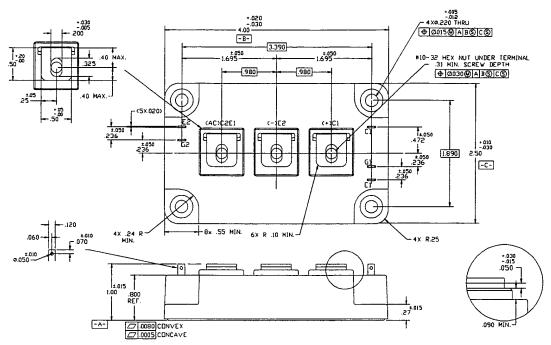


Fig. 3 - Test Waveforms for Circuit of Fig. 2, Defining  $\mathsf{E}_{\mathsf{on}},\,\mathsf{t}_{\mathsf{d}(\mathsf{on})},\,\mathsf{t}_{\mathsf{r}}$ 

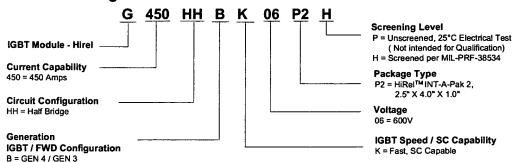


#### Case Outline and Dimensions - HiRel™ INT-A-Pak 2



Notes: 1) All dimensions are in inches 2) Unless otherwise specified, Tolerances .XX =  $\pm 0.01$ , .XXX =  $\pm 0.005$ 

#### Part numbering Nomenclature



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WORLD HEADQUARTERS: 233 Kansas St., El Segundo, California 90245, Tel: (310) 252-7105 IR LEOMINSTER: 205 Crawford St., Leominster, Massachusetts 01453, Tel: (978) 534-5776 Data and specifications subject to change without notice. 11/05